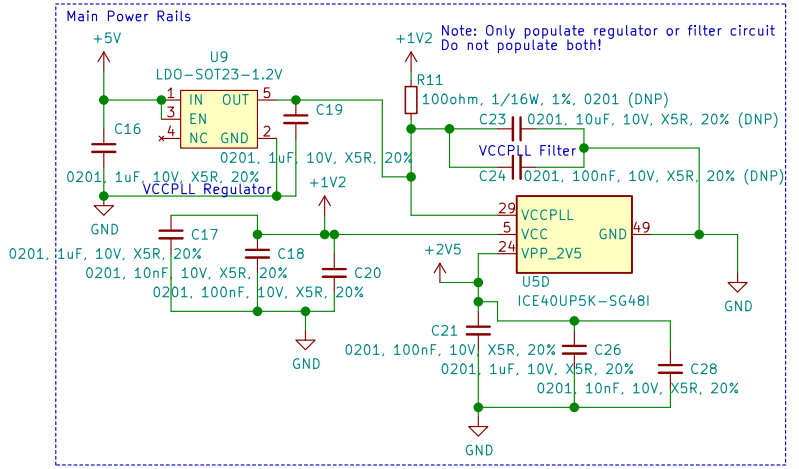
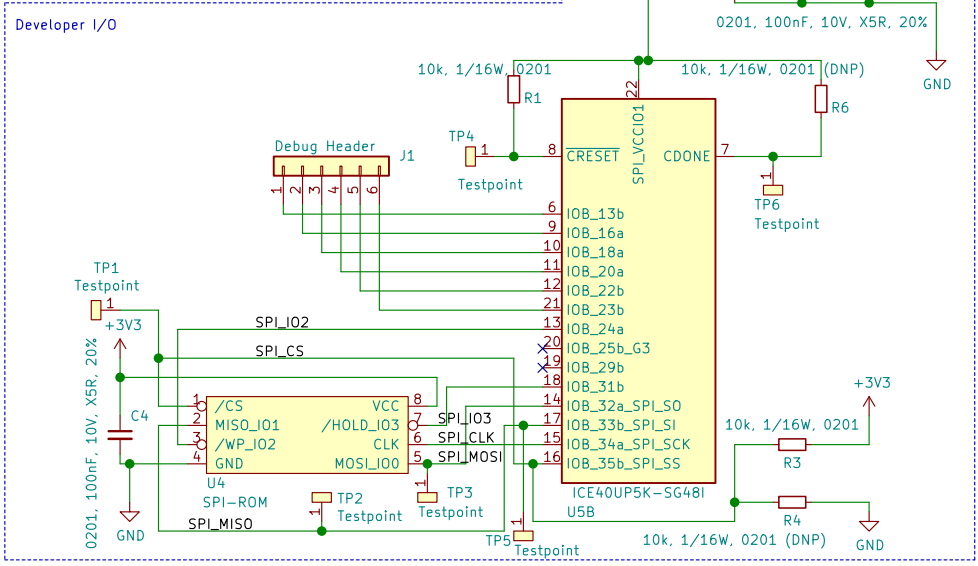
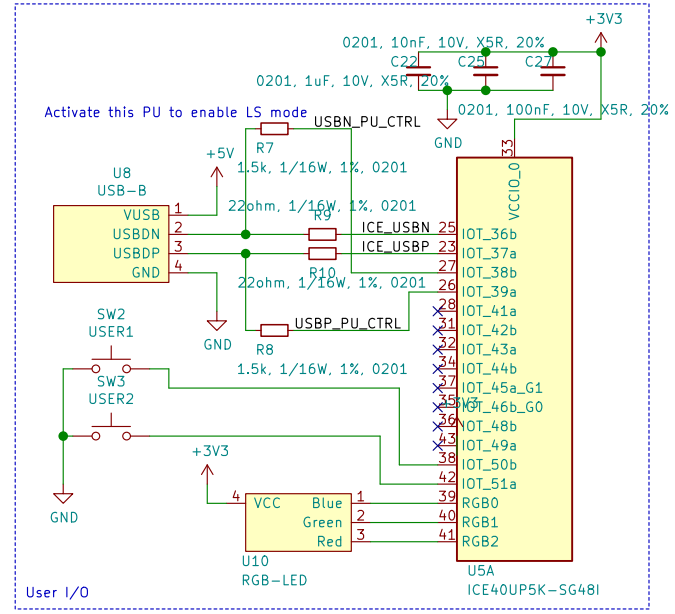
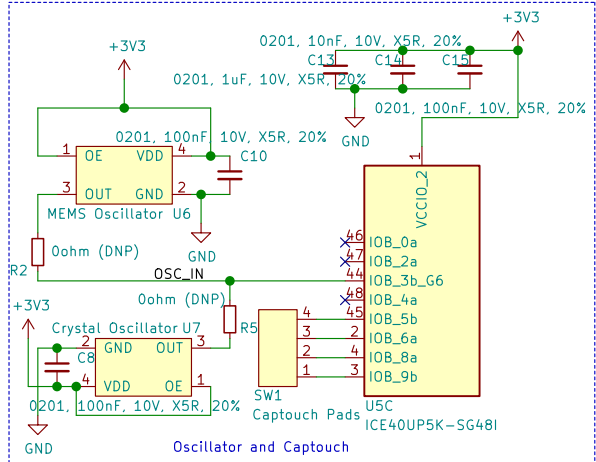


Power sequencing:
 1) 1.2V (VCC/VCCPLL)
 2) 3.3V (SPI/VCCIO1)
 3) 2.5V (VPP_2V5)
 Subsequent rails activate after previous rail goes >0.5V



Note: Only populate regulator or filter circuit. Do not populate both!

Stretch PCB for electrical verification	
Foosn PTE Ltd.	
Sheet: /	
File: tomu-fpga.sch	
Title: Tomu FPGA	
Size: A4	Date: 2018-10-30
KiCad E.D.A. kicad (5.0.0)	Rev: EVT1
	Id: 1/1